

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins
Term:	L25 and 124 <div style="float: right; text-align: center;"> <div style="border: 1px solid black; width: 15px; height: 15px; margin: 0 auto; line-height: 15px;">▲</div> <div style="border: 1px solid black; width: 15px; height: 15px; margin: 0 auto; line-height: 15px;">▼</div> </div>
Display:	<div style="display: flex; align-items: center; gap: 10px;"> <div style="border: 1px solid black; padding: 2px 10px;">10</div> <div>Documents in <u>Display Format:</u> <div style="border: 1px solid black; padding: 2px 10px;">-</div></div> <div>Starting with Number <div style="border: 1px solid black; padding: 2px 10px;">1</div></div> </div>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search

Clear

Interrupt

Search History

DATE: Thursday, February 16, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> <u>Query</u> <small>side by side</small>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> <small>result set</small>
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L26</u> L25 and 124	8	<u>L26</u>
<u>L25</u> ('no' adj2 address) (12 or memory) same 11 same (host or CPU or processor) same (bit or identifier) same (indicate or indicating or identifying or identify) same (destination or source or device) same (instruction or code) same (receive or receiving or execute or executing)	11861	<u>L25</u>
<u>L24</u> L21 and 112 and 117	230	<u>L24</u>
<u>L23</u> L21 or 112 or 117	0	<u>L23</u>
<u>L22</u> L20 not 119	141	<u>L22</u>
<u>L21</u> mclean.xp.	1	<u>L21</u>
<u>L20</u> mclean-mayo.xp.	107	<u>L20</u>
<u>L19</u> mclean-mayo.pa.	106	<u>L19</u>
<u>L18</u> mclean-mayo.xa.	0	<u>L18</u>
<u>L17</u> pragmatic adj2 bit	5	<u>L17</u>
<u>L16</u> L14.xa.	3	<u>L16</u>
<u>L15</u>	0	<u>L15</u>

<u>L14</u>	executable adj instruction	32733	<u>L14</u>
<u>L13</u>	L12 and l11	1	<u>L13</u>
<u>L12</u>	mclean.xa.	140	<u>L12</u>
<u>L11</u>	cache adj2 control adj2 instruction	138	<u>L11</u>
<u>L10</u>	L8 same l7	12	<u>L10</u>
<u>L9</u>	L8 and l7	106	<u>L9</u>
<u>L8</u>	(indicate or indicating or identifying or identify) same first or second same (execute or executing) same (instruction or code)	594853	<u>L8</u>
<u>L7</u>	((append or appending or attach or attaching) adj4 bit adj4 (instruction or code))	193	<u>L7</u>
<u>L6</u>	((append or appending or attach or attaching) adj4 bit adj4 (instruction or code)) same (identify or identifier or identifying) same (source or destination)	3	<u>L6</u>
<u>L5</u>	((append or appending or attach or attaching) adj4 bit adj (instruction or code)) same (identify or identifier or identifying) same (source or destination)	0	<u>L5</u>
<u>L4</u>	L3 same (l2 or memory) same l1	161	<u>L4</u>
<u>L3</u>	(instruction or code) same (bit) same (indicate or indicating or identify or identifying) same (destination)	3135	<u>L3</u>
<u>L2</u>	nonvolatile ram or NVRAM	7899	<u>L2</u>
<u>L1</u>	controller or (control adj unit)	1784512	<u>L1</u>

END OF SEARCH HISTORY